

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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AP/2186

Application No.: 09/939,277 Filed: August 24, 2001

Inventor(s):

Paul Jeffrey Garnett Stephen Rowlinson Jeremy Graham Harris

Title:

Processor State

Reintegration Using Bridge

Direct Memory Access

Controller

Examiner:

Kim, Hong Chong

Group/Art Unit:

2186

Atty. Dkt. No: 5681-02700

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

010 ...

B. Noël Kivlin
Printed Name

Signature

8-2-04 Date

RESPONSE TO OFFICE ACTION OF June 2, 2004

RECEIVED

AUG 0 9 2004

Technology Center 2100

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of June 2, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.